



#6/Amend B
(WES)
11/2/01
Please amend
11/1/01

Docket No.: M4065.0127/P127-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Kie Y. Ahn, et al.

Application No.: 09/660,324

Group Art Unit: 2823

Filed: September 12, 2000

Examiner: E. Toledo

For: SILICON MULTI-CHIP MODULE
PACKAGING WITH INTEGRATED
PASSIVE COMPONENTS AND METHOD
OF MAKING

TECHNOLOGY CENTER 2800
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RESPONSE

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Commissioner for Patents

Washington, DC 20231

Dear Sir:

In response to the Office Action dated September 26, 2001, applicants respectfully submit the following:

In the claims:

Please cancel claim 89.

Please amend claim 88 as follows:

88 (amended) A process for forming an interposer element for use as a chip carrier comprising the steps of:

providing an insulating layer on at least one surface of a silicon substrate; and